data into desired bits.

[0015] In the controller-driver, the different image data is specified by the image data that is next to a current screen in consecutive screens or that is obtained by carrying out predetermined processing in connection with the image data stored as a part of the image data formed by the plurality of bits.

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[0016] The controller-driver comprises a color decrease process circuit that is operable in the second mode to execute bit-number conversion of input image data of the plurality of bits and the color decrease process into a prescribed number of bits to produce the prescribed number of bits as the part of the plurality of bits.

[0017] The color decrease circuit in the controller-driver is structured by a dither circuit for carrying out dither processing.

[0018] The controller-driver comprises a processing circuit operable in the second mode to carry out a predetermined process in connection with the input image data of the plurality of bits to produce processed image data as a part of the plurality of bits or to carry out the predetermined process in connection with the image data output from the color decrease circuit to produce as a part of the plurality of bits.

[0019] In the controller-driver, the processing circuit is operable in the second mode to carry out the predetermined processing in connection with the image data of a part of the plurality of bits stored in the memory area and a plurality of input bits or in connection with the image data output from the color decrease circuit and stores, into the empty area, processed image data as the part of the plurality of bits.

[0020] In the controller-driver, the memory area is divided into a plurality of subdivided memory areas that are equal to the bit number providing the gradation levels of the image data given by the plurality of bits. The first mode is for storing the whole of the image data by dividing the image data of the plurality of bits at every one of desired bit numbers in correspondence with a division factor of